

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Shunpei YAMAZAKI et al.) Attention: DO/EO/US
Based on PCT/JP2004/018978)
Filed: December 14, 2004)
For: METHOD FOR MANUFACTURING)
THIN FILM INTEGRATED CIRCUIT)
DEVICE, NONCONTACT THIN FILM)
INTEGRATED CIRCUIT DEVICE)
AND METHOD FOR)
MANUFACTURING THE SAME, AND)
IDTAG AND COIN INCLUDING THE)
NONCONTACT THIN FILM)
INTEGRATED CIRCUIT DEVICE)

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the

requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

U.S. Patent Publication No. 2001/0015256 is in the family of JP 2001-272923.

U.S. Patent Publication No. 2001/0053559 is in the family of JP 2001-284342.

U.S. Patent No. 6,887,650 and U.S. Patent Publication Nos. 2003/0022403 and 2005/0106839 are in the family of WO 2003-010825.

U.S. Patent No. 6,168,829 and EP 0 607 709 are in the family of JP 06-299127.

U.S. Patent Nos. 5,378,536 and 5,389,438 and EP 0 443 263 are in the family of JP 2992092.

EP 1 193 759 is in the family of JP 2002-343877 and WO 2000/051181.

Respectfully submitted,



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Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	
				Filing Date	
				First Named Inventor	
				Group Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	
				0756-7719	

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
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		6,887,650		Shimoda et al.	05/03/2005	
		2003/0022403		Shimoda et al.	01/30/2003	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	2001-272923			10/05/2001	Abst.
		JP	2001-284342			10/12/2001	Abst.
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Leading Trends, "Sense of Crisis" is a Trigger. Ignited Evolution of a Sesame- Grain Sized Chip Technology Development is Entering into the Second Phase, Nikkei Electronics, November 18, 2002, pp. 67-76	Full

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